

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|--------|---|---|------------------|---------|------------------|
| L1 | 511570 | (electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:38 |
| L2 | 11019 | voltage adj regulation | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:38 |
| L3 | 1349 | L1 and L2 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:39 |
| L4 | 57 | 3 and (current adj sink\$3) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:40 |
| L5 | 45 | 4 and @ad<="20030228" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:40 |
| L6 | 0 | 5 and (program\$3 adj load adj circuit) and memory | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:41 |
| L7 | 0 | ((non adj volatile adj memory) and (voltage adj regulation adj system) and (multiword adj programming) and (corresponding adj circuits) and (memory adj cell adj content)and (current adj sinking)).clm. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:44 |
| L8 | 21 | martines-ignazio.in. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:44 |
| L9 | 3 | scardaci-massimo.in. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:45 |

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| S1 | 480876 | (electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 13:45 |
| S2 | 10582 | voltage adj regulation | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:06 |
| S3 | 1243 | S1 and S2 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 13:44 |
| S4 | 14 | S3 and (memory adj cell adj matrix) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 13:57 |
| S5 | 69 | S3 and (column adj decoder) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 13:56 |
| S6 | 78 | S3 and ((memory adj cell adj matrix) or (column adj decoder)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:06 |
| S7 | 36 | (program\$3 adj load adj circuit) and memory | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/19 17:40 |
| S8 | 3 | (non adj volatile) and falsh | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/30 16:30 |
| S9 | 46361 | (non adj volatile) and flash | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:05 |

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| S10 | 12273 | S9 and eras\$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 16:24 |
| S11 | 11157 | S10 and read\$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 16:25 |
| S12 | 8649 | S11 and writ\$5 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 16:25 |
| S13 | 178 | S12 and (program\$3 adj load) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 16:25 |
| S14 | 110 | S13 and ground | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 16:31 |
| S15 | 69 | S14 and active | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/18 16:27 |
| S16 | 3 | "6603681" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/30 16:30 |
| S17 | 3 | (non adj volatile) and falsh | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 14:30 |
| S18 | 483783 | (electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:04 |

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| S19 | 1206 | 365/185.21 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:06 |
| S20 | 24 | S19 and (voltage adj regulation) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:06 |
| S21 | 467 | S19 and ((non adj volatile) and flash) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S22 | 158 | S21 and ((memory adj cell adj matrix) or (column adj decoder)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:06 |
| S23 | 7 | S22 and (voltage adj regulation) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:06 |
| S24 | 2314 | 365/185.18 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S25 | 57 | S24 and (voltage adj regulation) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S26 | 26 | S25 and ((non adj volatile) and flash) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S27 | 777 | 365/185.19 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S28 | 11 | S27 and (voltage adj regulation) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |

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| S29 | 1156 | 365/185.09 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S30 | 455 | S29 and ((non adj volatile) and flash) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |
| S31 | 4 | S30 and (voltage adj regulation) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/01 15:07 |